

IN THE ABSTRACT OF THE DISCLOSURE

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Abstract of the Disclosure

A variety of embodiments may include a phase lock loop, perhaps as part of an integrated circuit. In some embodiments, the phase lock loop may include a voltage-to-current circuit to influence a voltage on a capacitor, and a voltage controlled oscillator responsive to the voltage on the capacitor. In some embodiments, the phase lock loop may include a voltage controlled oscillator to generate a differential signal, and a linear voltage-to-current converter responsive to samples of the differential signal to create a control voltage for the voltage controlled oscillator.